

# PATENT ABSTRACTS OF JAPAN

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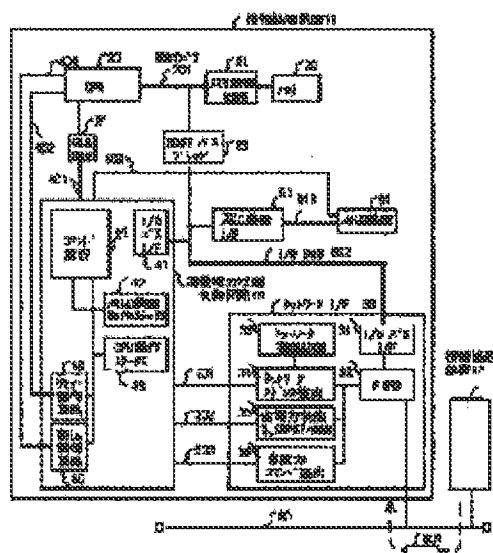
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## (54) NETWORK SYSTEM WITH POWER-SAVING FUNCTION

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide the system which can controls a power-saving state without placing a sequential information processor in operation from a sleep state by using a packet for power-saving state control, analyzing the command of the packet without CPU's participation, and controlling equipment when power-saving control over the information processor is performed through a LAN(local area network).

**SOLUTION:** A 2nd information processor 12 sends a packet 901 requesting state transition to the information processor 11 in a power-saving state through the network 90. The information processor is in the sleep state wherein the clock of the CPU 20 is stopping. A network I/F 30 acquires a power-saving command 303 from the packet 901 stored in an FIFO 32 and a power-saving control circuit 40 varies the clock frequency of



the CPU and controls a peripheral device 61 in the sleep state according to the command 303.